

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2030	bias transistor base dc	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	WITH	ON	2006/09/28 10:52
L2	6959	bias transistor base dc	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	ON	2006/09/28 10:52
L3	637	(341/58,95).CCLS.	USPAT	OR	OFF	2006/09/28 10:53
L4	65	(341/58).CCLS.	US-PGPUB	OR	OFF	2006/09/28 10:53
L5	0	l1 and l4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2006/09/28 10:57
L6	3298	signal buffer track DC flips "zero"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2006/09/28 10:58
L7	5	signal buffer track DC flips "zero"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	ON	2006/09/28 10:58
L8	0	signal buffer track DC flips "zero"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	WITH	ON	2006/09/28 10:58